### DECCAN COLLEGE OF ENGINEERING

**EXAMINATION TIME TABLE**

**Time:** 2.00 PM To 5.00 PM

<table>
<thead>
<tr>
<th>Date &amp; Day</th>
<th>CIVIL ENGINEERING (Transportation Engg.)</th>
<th>ELECTRONICS AND COMMUNICATION ENGINEERING (Digital Systems)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Regular II - Semester</td>
<td>Regular II – Semester</td>
</tr>
<tr>
<td>26-12-2011 MONDAY</td>
<td>Design of Highway Infrastructure</td>
<td>= = = = = = =</td>
</tr>
<tr>
<td>28-12-2011 WEDNESDAY</td>
<td>Rural &amp; Regional Transportation Systems</td>
<td>Data and Computer Communication Networks</td>
</tr>
<tr>
<td>30-12-2011 FRIDAY</td>
<td>Rural Roads</td>
<td>Digital Design &amp; PLDs</td>
</tr>
<tr>
<td>02-01-2012 MONDAY</td>
<td>Pavement Systems Engg.</td>
<td>Advanced Computer Organization</td>
</tr>
<tr>
<td>04-01-2012 WEDNESDAY</td>
<td>GIS &amp; GPS Application to Transportation Engineering</td>
<td>Coding Theory &amp; Techniques</td>
</tr>
<tr>
<td>06-01-2012 FRIDAY</td>
<td>Transportation Modelling and Simulation</td>
<td>Adv. Digital Design with Verilog HDL</td>
</tr>
<tr>
<td>09-01-2012 MONDAY</td>
<td>= = = = = =</td>
<td>Analog &amp; Mixed Signal IC Design</td>
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</tbody>
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**Note:** This Time-Table is also applicable for Pre-Ph.D. students registered for August 2011 Examination.

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