A Short Course on

REAL TIME SIGNAL PROCESSING (RTSP-12)
(with Digital Signal Processors, Blackfin and SHARC)

29-31, March 2012
(Course Code : NERTU/SC/50)

Coordinators

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and Technology for Women

ABOUT DEPARTMENT OF ECE, OU

The Department of Electronics and Communication Engineering was established in 1959. It bears the indelible stamp of its founder late Prof. K. Krishnan Nair. It has a unique distinction of obtaining ‘A’ grade accreditation from NBA, AICTE and ISO certifications. Recognized as best participating institution under the NETWORK project, funded by Swiss Development Corporation. The Department has been in the forefront for Research and Training activities in Digital Signal Processing, Communications and Microwave Engineering. Many projects sponsored by CSIR, DST(FIST), Ministry of HRD, AICTE, Space application Center, DRDO, HBKNIFE have been successfully completed by the Department. The alumina of this department has occupied distinguished posts not only in India but also abroad. Presently, the Department is offering one UG, 4 PG and Ph.D. programs.

ABOUT NERTU

The Research and Training Unit for Navigational Electronics (NERTU), established in 1982, is the focal point for research and training in navigational electronics in India. Since its inception, NERTU has successfully executed 40 sponsored/consultancy projects funded by DRDO, ISRO, DST, MIT, ECIL, HAL, BEL, AICTE and ASL. Currently, several projects in different areas related to navigation, signal processing and communications are in progress. It has also conducted 49 short term courses/workshops/conferences on various topics of Navigation.

DSPS IN MULTIMEDIA and COMMUNICATION ENGINEERING

The demand for Multimedia systems including PCs, set top boxes, portable digital audio (MP3, DVD) players, digital TVs, and digital radios is increasing day by day. This rapid development is possible due to the development in the Digital Signal Processors (DSPs) and Communication Technology. Every year some new algorithms/standards are emerging and there will be some modifications/enhancements to the existing standards for multimedia and other applications. There is a relative dearth of coders. According to Chris Lanfear, director of the embedded software and tools division of research firm Venture Development Corp, (Natick, Mass.), “The number of lines of code is increasing at a CAGR [compound annual growth rate] of 46 percent, but the number of developers is only rising at the rate of 7.5 percent”. Therefore, there is a need for trained people on Digital Signal Processors to develop the upcoming/modified algorithms on DSPs.

ABOUT MMADSPs LABORATORY

By recognizing the potential growth and the need of DSP processors in Multimedia applications and research, Analog Devices came forward to sponsor the Multimedia and ADSPs (MMADSPs) Laboratory at Department of ECE, University College of Engineering, Osmania University, to create professional quality Manpower and promote training, research activities in the broad area of Multimedia and Digital Signal Processors. As part of this project, ADI is sponsoring two full-time research fellowships, Blackfin and SHARC evaluation boards and the required VDSP software. Under this project, earlier three courses were conducted and this is the fourth course in developing quality manpower in the area of multimedia and digital signal processors.

REGISTRATION FEE:
Rs. 3000/- for engineers/scientists from industries/research organizations
Rs. 2000/- for teachers/academicians
Rs. 1000/- Full-Time Students
The fee includes lecture notes, working lunch, snacks and tea. The candidates have to make their own arrangements for accommodation.

Seats are limited and filled based on First come first served. Interested candidates can download the registration form from www.osmania.ac.in or from www.uceou.edu and send the filled registration form along with DD/Cheque drawn in favor of “The Chief-Coordinator, Multimedia and ADSPs Laboratory, Dept. of ECE, OU”.

Last date for receiving the applications is 17th March 2012.

Email Ids/Phone Numbers for correspondence:

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N. Rajaiah, Ph :040-27098213
### Proforma for Registration

A short Course on

**Real Time Signal Processing**
*(Blackfin and Other Processors)*

**RTSP-12**  
*(Course Code: NERTU/SC/50)*  
29-31 March 2012

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**Place**  

**Date**  

Signature of the Sponsoring Authority/Candidate